

Abstracts

Computer-Aided Design of Monolithic Distributed Amplifiers with Yield Optimization

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A CAD procedure for monolithic distributed amplifiers with yield optimization is presented. Considering the difficulty of fabricating exact circuit parameters with the present technology, a yield optimization, which optimizes the predicted yield rate of circuits produced when circuit elements deviate from their nominal values, has been integrated into this CAD process. Comparing a distributed amplifier designed without yield optimization and one generated with yield optimization in this procedure, the predicted yield rate has been increased from 30% to 50%.

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